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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/687,784

10/17/2003

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RAZA-05000

9860

76265 7590 10/15/2008  
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EXAMINER

ANDREWS, LEON T

ART UNIT

PAPER NUMBER

2416

MAIL DATE

DELIVERY MODE

10/15/2008

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/687,784	<b>Applicant(s)</b> SINGH ET AL.	
	<b>Examiner</b> LEON ANDREWS	<b>Art Unit</b> 2616	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 21 July 2008.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1,2,4-11,14-19,21 and 23-26 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2,4-11,14-19,21 and 23-26 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)          | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***RCE***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on July 21, 2008 has been entered.

- **Claims 1, 7-11, 15-19, 21 and 25-26** were amended.
- **Claims 3, 12-13, 20 and 22** were cancelled.

### ***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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**Claims 1, 10-11, 19, 21, 25 and 26** are being rejected under 35 U.S.C. 103(a) as being unpatentable over Wong (Pub. No.: 2004/0264464 A1 using Provisional application No.: 60/482,759) in view of Tang et al. (Patent No.: US 6,553,028 B1).

**Regarding Claim 1, Wong** discloses a multicast packet duplication system for multicast packets (Internet Protocol Multicast (IPMC) packet duplication covers tables required to implement the MMU and egress module, page 3, lines 2-4) containing at least multicast address data (multicast packet is replaced with source MAC address, page 3, lines 14-15), comprising:

an input port (Block Diagram, CPI ingress bus, page STN-2) configured to receive a packet (IPMC packet, page 5, line 21); and

a plurality of output ports (Fig. 2, multicast packet generated and routed to RCVs 204-210 by way of router 250, column 8, lines 64-65; ports of the egress VLAN that receive the frame, column 14, lines 30-31) configured to output the packet, wherein:

a number of duplications of the packet for each of the plurality of output ports is controlled by descriptors (replication engine includes the pointer and index where the index (descriptor) enables the replication engine to perform multicast packet replication, further specifies the port with the incoming (ingress) VLAN and rewrites the frame (hashing) destined to ports on the VLAN (output ports) other than the ingress VLAN, column 14, lines 4-15) by a hashing function (each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62) applied to said multicast address data; wherein an encoding format of the descriptors include at least one of:

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a contiguous range encoding that includes a starting indicator and an ending indicator (replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43); or

a non-contiguous range encoding that includes a most significant bit (MSB) portion of an indicator and a bitmap decoded from a least significant bit (LSB) portion of the indicator;

and

a discrete encoding that includes a first indicator and a second indicator.

Wong teaches the limitations of the claims including multicast packet duplicating system. But, Wong fails to specifically teach plurality of output ports, hashing function and continuous range that includes starting/ending indicators.

However, Tang et al. teaches Fig. 2, multicast packet generated and routed to RCVs 204-210 by way of router 250, column 8, lines 64-65; each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62), and replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to use Tang et al.'s plurality of output ports, hashing function and continuous range that includes a starting/ending indicators because this would have allowed the

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switch to forward the routed multicast packet/frames to the RCVs 206-210 coupled to its ports, column 9, lines 9-11.

**Regarding Claims 2 and 14, Wong** discloses the packet duplication system and method (IPMC Replication steps, page STN-13), wherein: each of the number of duplications is coupled to a Virtual Local Area Network (VLAN) (IPM packet replication per VLAN, page STN-10, line 8).

**Regarding Claims 4 and 15, Wong** discloses the packet duplication system and method, wherein: the VLAN pointer descriptors arranged in the linked-list table include at least one shared descriptor (IPMC Replication, Head\_Pointer and the Next\_Pointer used as index to the LS table, step 5, STN-13).

**Regarding Claim 5, Wong** discloses the packet duplication system of claim 1, further comprising: a pointer table (ECMP Support, L3 Interface Table, page STN-7) having a width comprising a plurality of entries (column of 8 entries in the L3 table, ECMP Dest\_Ip Search, step 7, page STN-8) coupled to the linked-list table.

**Regarding Claim 6, Wong** discloses the packet duplication system of claim 5, wherein: each of the plurality of entries (column of 8 entries in the L3 table, ECMP Dest\_Ip Search, step 7, page STN-8) corresponds to one of the plurality of output ports (Block Diagram, CPE egress bus, page STN-2).

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**Regarding Claims 7 and 16, Wong** discloses the packet duplication system and method, wherein: the contiguous range encoding includes a starting Virtual Local Area Network (VLAN) indicator (IPMC Replication, VLAN\_ID1, step 9, page STN-13) and an ending VLAN indicator (IPMC Replication, VLAN\_ID2, step 12, page STN-13).

**Regarding Claims 8 and 17, Wong** discloses the packet duplication system and method, wherein: the non-contiguous range encoding includes a most significant bit (MSB) portion (IPMC Replication, 64-bit vector for specifying the MS (Most Significant) 6 bits of VLAN\_ID, page STN-11, lines 11-12) of a Virtual Local Area Network (VLAN) indicator (IPMC Replication, VLAN\_ID, page STN-11) and a bitmap (ECMP Dest\_Ip Search, step 7, LPM table get 12-bit L3\_table\_index with 3-bit count field, page STN-8) decoded from a least significant bit (LSB) portion (ECMP Dest\_Ip Search, step 8, index points to the first entry of column of 8-entries in the L3 table, page STN-8) of the VLAN indicator.

**Regarding Claims 9 and 18, Wong** discloses the packet duplication system and method, wherein: the discrete encoding includes a first Virtual Local Area Network (VLAN) indicator (IPMC Replication, VLAN\_ID1, step 9, page STN-13) and a second VLAN indicator (IPMC Replication, VLAN\_ID2, step 12, page STN-13).

**Regarding Claims 10 and 19, Wong** discloses the packet duplication system and method, wherein: the encoding format is configured to be selected in response to control bits (replication

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process for each outgoing VLAN starts from the pointer 750 until an entry having an asserted control bit specifies the termination of the replication for the frame, column 14, lines 37-43).

Wong teaches the limitations of the claims including multicast packet duplicating system. But, Wong fails to specifically teach plurality of output ports, hashing function and continuous range that includes starting/ending indicators.

However, Tang et al. teaches Fig. 2, multicast packet generated and routed to RCVs 204-210 by way of router 250, column 8, lines 64-65; each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62), and replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to use Tang et al.'s plurality of output ports, hashing function and continuous range that includes a starting/ending indicators because this would have allowed the switch to forward the routed multicast packet/frames to the RCVs 206-210 coupled to its ports, column 9, lines 9-11.

**Regarding Claim 11, Wong** discloses a method (IPMC Replication steps, page STN-13) of controlling a duplication of a multicast packet (Internet Protocol Multicast (IPMC) packet duplication covers tables required to implement the MMU and egress module, page 3, lines 2-4)



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containing at least multicast address data (multicast packet is replaced with source MAC address, page 3, lines 14-15), comprising:

receiving the packet (Block Diagram, CPI ingress bus, page STN-2);

performing a hashing function (each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62) on said multicast address data;

using the results of said hashing function as an index (where the multicast entry accesses an appropriate entry of the table using the destination address and the VLAN ID, columns 12-13, lines 66-67 and 1-2 respectively) for a linked-list table (ECMP Support, L3 table, page STN-7); said linked-list table including a plurality of pointers (Head\_Pointer and the Next\_Pointer used as index to the LS table, step 5, STN-13);

accessing a first multicast descriptor pointer (ECMP Dest\_Ip Search, LPM table 1<sup>st</sup>-searchkey=lpm\_addr [14:0] = {11 'h0, ip0, step 3, page STN-8} in said linked-list table; said multicast descriptor pointer pointing to multicast descriptors comprised of at least multicast Virtual Area Network (VLAN) pointers (ECMP Dest\_Ip Search, step 10, VLAN\_tag, page STN-8);

using at least one of said multicast VLAN pointers to access a multicast VLAN table (ECMP Dest\_Ip Search, step 10, L3 Interface Table, page STN-8) comprised of a second pointers to VLAN pointer descriptors (ECMP Dest\_Ip Search, Next-searchkey=lpm\_addr [14:0] = {next\_pointer,lpn), step 5, page STN-8);

accessing a VLAN pointer descriptor (ECMP Dest\_Ip Search, step 10, VLAN\_tag, page STN-8) in response to the second pointer; and using information contained in said VLAN pointer

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descriptor to control applying an encoding for the duplication of the packet (replication engine includes the pointer and index where the index (descriptor) enables the replication engine to perform multicast packet replication, column 14, lines 4-10);

wherein applying the encoding includes selecting a format of descriptors, the format including at least one of:

a contiguous range encoding that includes a starting indicator and an ending indicator (replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43); or

a non-contiguous range encoding that includes a most significant bit (MSB) portion of an indicator and a bitmap decoded from a least significant bit (LSB) portion of the indicator; and

a discrete encoding that includes a first indicator and a second indicator.

Wong teaches the limitations of the claims including multicast packet duplicating system. But, Wong fails to specifically teach plurality of output ports, hashing function and continuous range that includes starting/ending indicators.

However, Tang et al. teaches Fig. 2, multicast packet generated and routed to RCVs 204-210 by way of router 250, column 8, lines 64-65; each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62), and replication process for each outgoing VLAN starts from the pointer 750

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(starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to use Tang et al.'s plurality of output ports, hashing function and continuous range that includes a starting/ending indicators because this would have allowed the switch to forward the routed multicast packet/frames to the RCVs 206-210 coupled to its ports, column 9, lines 9-11.

**Regarding claims 21 and 25 Wong** discloses a multicast packet duplication system for multicast packets (Internet Protocol Multicast (IPMC) packet duplication covers tables required to implement the MMU and egress module, page 3, lines 2-4) containing at least multicast address data (multicast packet is replaced with source MAC address, page 3, lines 14-15), comprising:

an input port (Block Diagram, CPI ingress bus, page STN-2) configured to receive a packet (IPMC packet, page 5, line 21); and

a plurality of output ports (Block Diagram, CPE egress bus, page STN-2) configured to output the packet; said output ports being coupled to one or more Virtual Local Area Networks (VLAN) (VLAN) (IPM packet replication per VLAN, page STN-10, line 8);

wherein said system applies a hashing function (each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62) to the multicast address data of said multicast packets; and

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wherein said system uses the result of said hashing function as an index (where the multicast entry accesses an appropriate entry of the table using the destination address and the VLAN ID, columns 12-13, lines 66-67 and 1-2 respectively) to a linked- list table (ECMP Support, L3 table, page STN-7); said linked-list table having entries that comprise either multicast descriptors or pointers (Head\_Pointer and the Next\_Pointer used as index to the LS table, step 5, STN-13) to multicast descriptors;

said multicast descriptors being comprised of at least multicast VLAN descriptors or pointers (ECMP Dest\_Ip Search, step 10, VLAN\_tag, page STN-8) to multicast VLAN descriptors; wherein a number of distributions of said multicast packet and an output port distribution of said multicast packet is controlled by information stored in either the multicast descriptors or multicast VLAN descriptors (replication engine includes the pointer and index where the index (descriptor) enables the replication engine to perform multicast packet replication, further specifies the port with the incoming (ingress) VLAN and rewrites the frame destined to ports on the VLAN (output ports) other than the ingress VLAN, column 14, lines 4-15); wherein said multicast VLAN descriptors contain a plurality of entries (column of 8 entries in the L3 table, ECMP Dest\_Ip Search, step 7, page STN-8) each describing the multicast packet distribution to a different VLAN (VLANs, page 3, lines 6-7); and

wherein said VLAN descriptors include at least one of:

a contiguous range encoding that includes a starting VLAN indicator and an ending VLAN indicator (replication process for each outgoing VLAN starts from the pointer 750 (starting

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indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43); or  
a non-contiguous range encoding that includes a most significant bit (MSB) portion of a VLAN indicator and a bitmap decoded from a least significant bit (LSB) portion of the VLAN indicator;  
and  
a discrete encoding that includes a first VLAN indicator and a second VLAN indicator.

Wong teaches the limitations of the claims including multicast packet duplicating system. But, Wong fails to specifically teach plurality of output ports, hashing function and continuous range that includes starting/ending indicators.

However, Tang et al. teaches Fig. 2, multicast packet generated and routed to RCVs 204-210 by way of router 250, column 8, lines 64-65; each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62), and replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to use Tang et al.'s plurality of output ports, hashing function and continuous range that includes a starting/ending indicators because this would have allowed the switch to forward the routed multicast packet/frames to the RCVs 206-210 coupled to its ports, column 9, lines 9-11.

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**Regarding Claim 23, Wong** discloses the packet duplication system of claim 21, wherein said multicast descriptors also include a multicast packet time to live field (packet aging based on packet time stamp, page STN-49, line 16).

**Regarding Claim 24, Wong** discloses the packet duplication system of claim 21, wherein said multicast Virtual Local Area Network (VLAN) descriptors contain a plurality of entries (column of 8 entries in the L3 table, ECMP Dest\_Ip Search, step 7, page STN-8) each describing the multicast packet distribution to a different VLAN (VLANs, page 3, lines 6-7).

**Regarding Claim 26, Wong** discloses a method (IPMC Replication steps, page STN-13) of controlling a duplication of one or more multicast packets (Internet Protocol Multicast (IPMC) packet duplication covers tables required to implement the MMU and egress module, page 3, lines 2-4) containing at least multicast address data (multicast packet is replaced with source MAC address, page 3, lines 14-15), comprising:

receiving (Block Diagram, CPI ingress bus, page STN-2) the multicast packet;

applying a hashing function (each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62) to the multicast address data of said multicast packets;

using the result of the hashing function as an index (ECMP Support, L3 interface index, page STN-7) to a linked-list table (ECMP Support, L3 table, page STN-7);

retrieving a multicast descriptor (Head\_Pointer and the Next\_Pointer used as index to the LS table, step 5, STN-13) from said linked-list table;

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using said multicast descriptor to find the multicast packet time to live data (packet aging based on packet time stamp, page STN-49, line 16) and a Virtual Local Area Network (VLAN)

descriptor (ECMP Dest\_Ip Search, step 10, VLAN\_tag, page STN-8)

obtaining information regarding how said multicast packets should be distributed to various output ports to at least one VLAN from said VLAN descriptor; and

using this distribution information to distribute said multicast packets to said at least one VLAN (replication engine includes the pointer and index where the index (descriptor) enables the replication engine to perform multicast packet replication, further specifies the port with the incoming (ingress) VLAN and rewrites the frame destined to ports on the VLAN (output ports) other than the ingress VLAN, column 14, lines 4-15);

wherein said VLAN descriptors include at least one of:

a contiguous range encoding that includes a starting VLAN indicator and an ending VLAN indicator (replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43); or

a non-contiguous range encoding that includes a most significant bit (MSB) portion of a VLAN indicator and a bitmap decoded from a least significant bit (LSB) portion of the VLAN indicator;

and

a discrete encoding that includes a first VLAN indicator and a second VLAN indicator.

Wong teaches the limitations of the claims including multicast packet duplicating system. But, Wong fails to specifically teach plurality of output ports, hashing function and continuous range that includes starting/ending indicators.

However, Tang et al. teaches Fig. 2, multicast packet generated and routed to RCVs 204-210 by way of router 250, column 8, lines 64-65; each multicast entry accessed by IP source address, IP destination address and the VLAN ID are hashed using a hash algorithm, column 12, lines 59-62), and replication process for each outgoing VLAN starts from the pointer 750 (starting indicator) until an entry having an asserted control bit (ending indicator) specifies the termination of the replication for the frame, column 14, lines 37-43.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to use Tang et al.'s plurality of output ports, hashing function and continuous range that includes a starting/ending indicators because this would have allowed the switch to forward the routed multicast packet/frames to the RCVs 206-210 coupled to its ports, column 9, lines 9-11.

### ***Citation of Pertinent Prior Art***

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Dobbins et al. (Patent Number: 5,684,800) discloses method for establishing restricted broadcast groups in a switched network.

Jain et al. (Patent Number: US 6,614,787 B1) discloses system and method for efficiently handling multicast packets by aggregating VLAN context.



Williams (Patent No.: US 6,775,283 B1) discloses passing VLAN information through descriptors.

Kaniz et al. (Patent No.: US 6,963,566 B1) discloses multiple address lookup engines running in parallel in a switch for a packet-switched network.

Boura et al. (Pub. No.: US 2002/0110139 A1) discloses logical multicast packet handling.

Wang (Patent No.: US 7,397,809 B2) discloses scheduling methods for combined unicast and multicast queuing.

Bender et al. (Pub. No.: US 2005/0080869 A1) discloses transferring message packets from a first node to a plurality of nodes in broadcast fashion via direct memory to memory transfer.

### ***Response to Arguments***

4. Applicant's arguments filed July 21, 2008 have been considered. However, in view of the grounds of rejections, the arguments are moot.

### ***Conclusion***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leon Andrews whose telephone number is (571) 270-1801. The examiner can normally be reached on Monday through Friday 7:30 AM to 5:00 PM EST.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rao S. Seema can be reached on (571) 272-3174. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

LA/la  
October 8, 2008

/Ian N. Moore/

Primary Examiner, Art Unit 2416